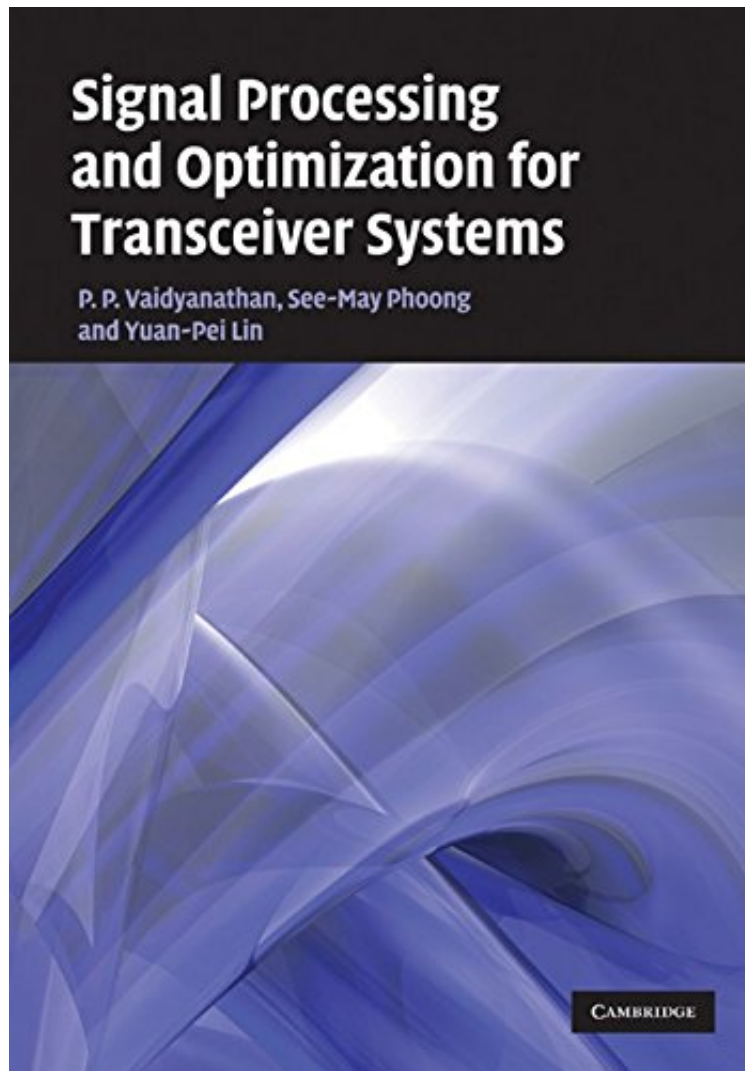


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# Signal Processing and Optimization for Transceiver Systems

*P. P. Vaidyanathan, See-May Phoong, Yuan-Pei Lin*  
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P. P. Vaidyanathan is a Professor of Electrical Engineering at the California Institute of Technology, where he has been a faculty member since 1983. He is an IEEE Fellow and has co-authored over 400 technical papers and two books in the area of signal processing. He has received numerous awards, including the Award for Excellence in Teaching at the California Institute of Technology three times.  
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